

80 V, 1 A NPN medium power transistors Rev. 1 — 23 June 2021

1. General description

NPN medium power transistors in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		PNP comlement
	Nexperia	JEDEC	
BCP56-Q	SOT223	SC-73	BCP53-Q
BCP56-10-Q]		BCP53-10-Q
BCP56-16-Q			BCP53-16-Q

2. Features and benefits

- High collector current capability ${\sf I}_{\sf C}$ and ${\sf I}_{\sf CM}$ •
- Three current gain selections
- High power dissipation capability
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Linear voltage regulators
- MOSFET drivers
- Low-side switches
- Power management •
- Amplifiers
- Battery-driven devices



4. Quick reference data

Table 2. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	80	V
I _C	collector current			-	-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	2	А
h _{FE}	DC current gain						
	BCP56-Q	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
	BCP56-10-Q		[1]	63	-	160	
	BCP56-16-Q		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

5. Pinning information

Table 3. Pinnin	Ig			
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	E	emitter		B - h
4	С	collector	∃ 1 ∃ 2 ∃ 3	E
				sym123

6. Ordering information

Table 4. Ordering information								
Type number	Package	Package						
	Name	Description	Version					
BCP56-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4	SOT223					
BCP56-10-Q		leads						
BCP56-16-Q								

7. Marking

Table 5. Marking	
Type number	Marking code
BCP56-Q	BCP56
BCP56-10-Q	BCP56/10
BCP56-16-Q	BCP56/16

8. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

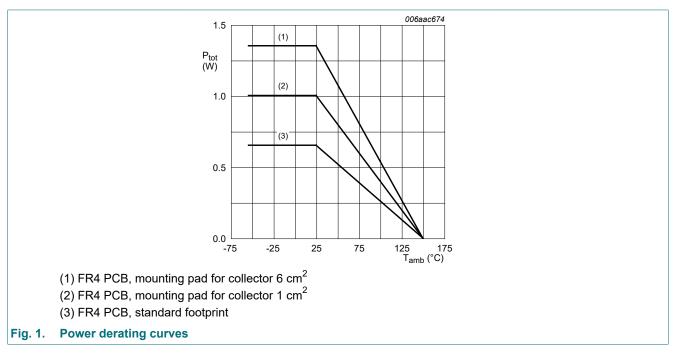
T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		100	V
V _{CEO}	collector-emitter voltage	open base		-	80	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current				1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		2	А
I _B	base current			-	0.3	А
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	0.3	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint. [1]

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm². [2]

[3]



9. Thermal characteristics

Table 7. Thermal characteristics

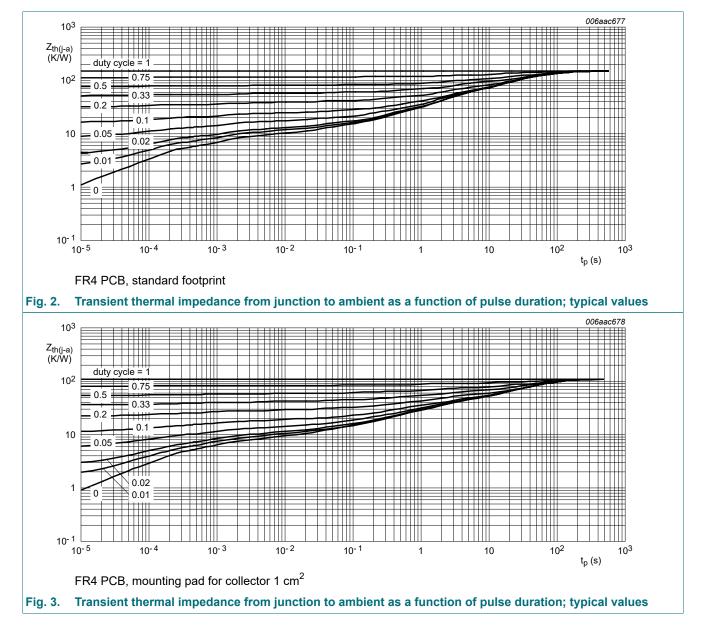
 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]			125	K/W
			[3]			93	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	16	K/W

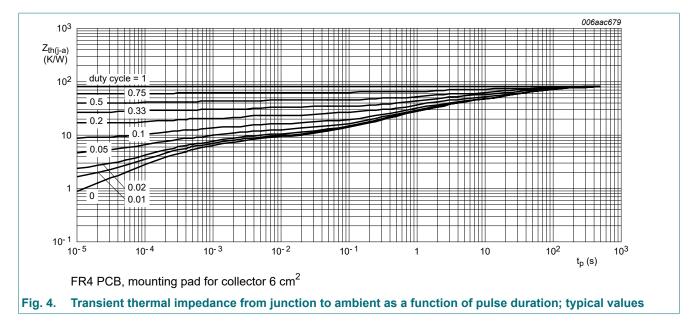
[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm².

[3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².



80 V, 1 A NPN medium power transistors



10. Characteristics

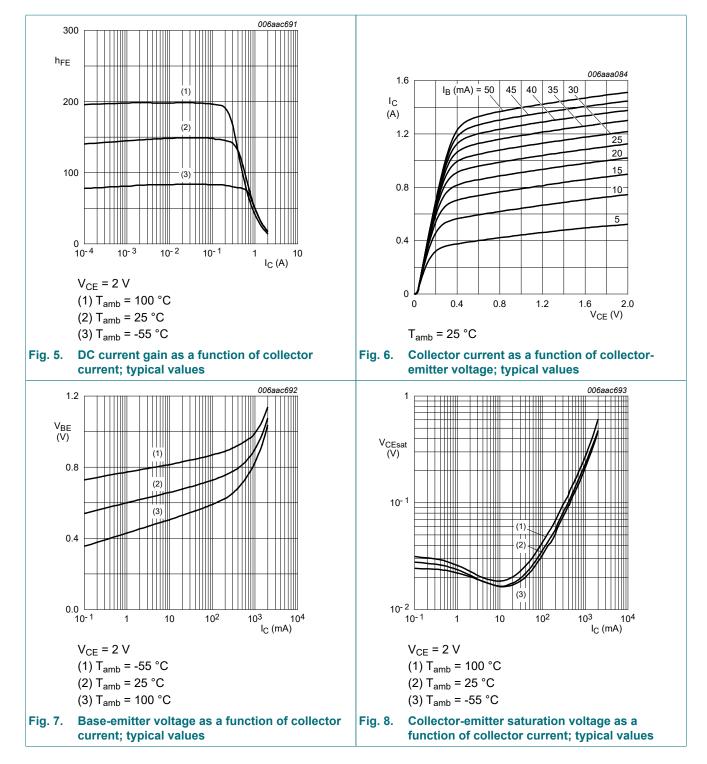
Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		100	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 10 mA; I _B = 0 A		80	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		5	-	-	V
I _{CBO}	collector-base	V _{CB} = 30 V; I _E = 0 A		-	-	100	nA
	cut-off current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain						_
BCP	BCP56-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCP56-10-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	160	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCP56-16-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA		100	-	250	
		V _{CE} = 2 V; I _C = 500 mA		40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	500	mV
V _{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA	[1]	-	-	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	6	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz		100	180	-	MHz

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

80 V, 1 A NPN medium power transistors

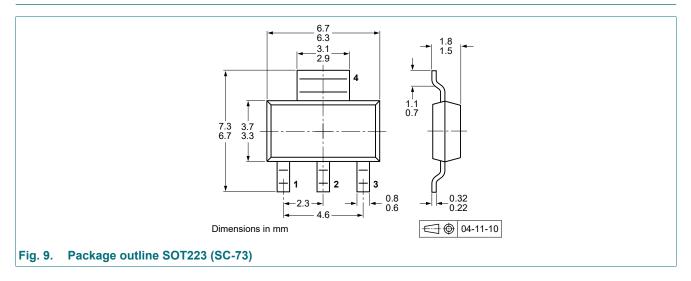


11. Test information

11.1. Quality information

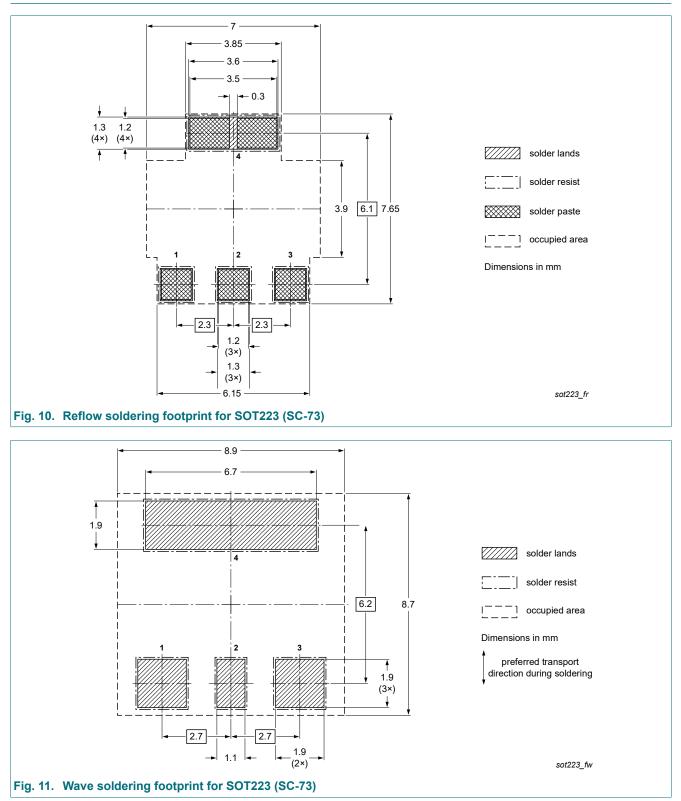
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



80 V, 1 A NPN medium power transistors

13. Soldering



14. Revision history

Table 9. Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP56-Q_SER v.1	20210623	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	2
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	3
9. Thermal characteristics	4
10. Characteristics	6
11. Test information	8
11.1. Quality information	8
12. Package outline	
13. Soldering	9
14. Revision history	10
15. Legal information	

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