

PDTC143ZM

50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k Ω , R2 = 47 k Ω 14 July 2023 Pr

Product data sheet

1. General description

NPN Resistor-Equipped Transistor (RET) in a leadless ultra small SOT883 (SC-101) Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA143ZM

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- · Reduces pick and place costs
- AEC-Q101 qualified

3. Applications

- Digital application in industrial segments
- Cost-saving alternative for BC847 series in digital applications
- Controlling IC inputs
- Switching loads

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
I _O	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	8	10	12	

[1] See "Section 11: Test information" for resistor calculation and test conditions.

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5. Pinning information

Table 2. Pinning information						
Pin	Symbol	Description	Simplified outline	Graphic symbol		
1	I	input (base)				
2	GND	ground (emitter)				
3	O	output (collector)	1 2 Transparent top view DFN1006-3 (SOT883)	GND sym007		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PDTC143ZM		plastic, leadless ultra small package; 3 terminals; 0.35 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	<u>SOT883</u>

7. Marking

Table 4. Marking codes	
Type number	Marking code
PDTC143ZM	E3

8. Limiting values

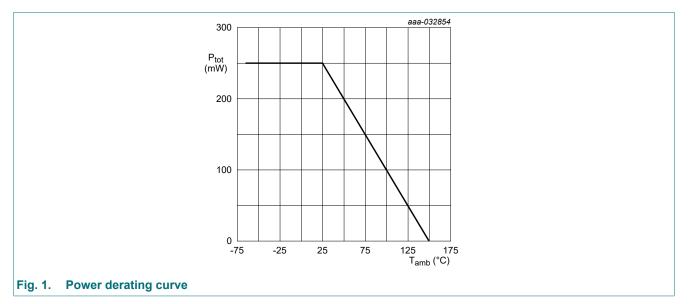
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
VI	input voltage			-5	30	V
lo	output current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1]

Reflow soldering is the only recommended soldering method. Device mounted on an FR4 Printed-Circuit Board (PCB) with 70 µm copper strip line, standard footprint. [2]



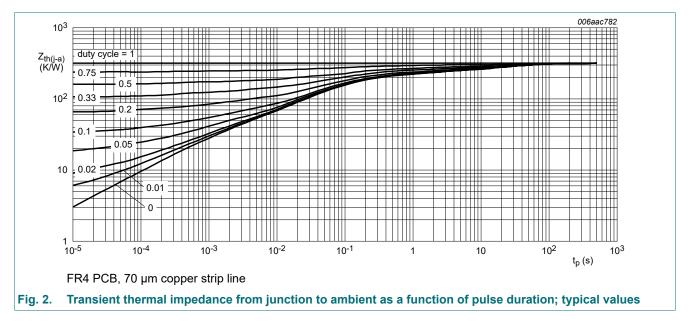
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W

[1] Reflow soldering is the only recommended soldering method.

[2] Device mounted on an FR4 PCB with 70 µm copper strip line, standard footprint.

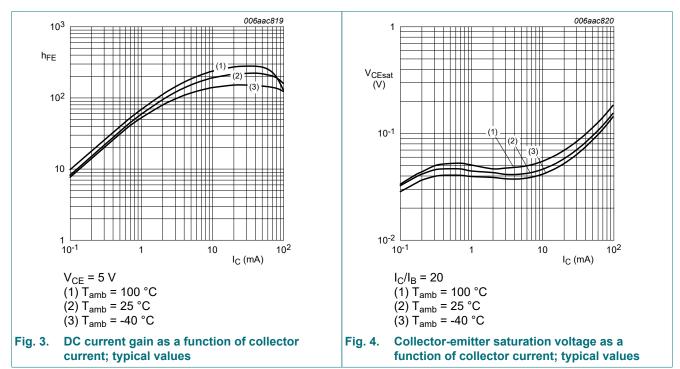


10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C		50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A; T _{amb} = 25 °C		50	-	-	V
I _{CBO}	collector-base cut-off current	$V_{CB} = 50 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{amb} = 25 ^{\circ}\text{C}$		-	-	100	nA
I _{CEO}	collector-emitter cut-off	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	100	nA
	current	V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C		-	-	5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$		-	-	170	μA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C		100	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 5 mA; I _B = 0.25 mA; T _{amb} = 25 °C		-	-	100	mV
V _{I(off)}	off-state input voltage	V _{CE} = 5 V; I _C = 100 μA; T _{amb} = 25 °C		-	0.6	0.5	V
V _{I(on)}	on-state input voltage	V_{CE} = 0.3 V; I _C = 5 mA; T _{amb} = 25 °C		1.3	0.9	-	V
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	8	10	12	
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	-	2.5	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	[2]	-	230	-	MHz

[1] See "Section 11: Test information" for resistor calculation and test conditions.

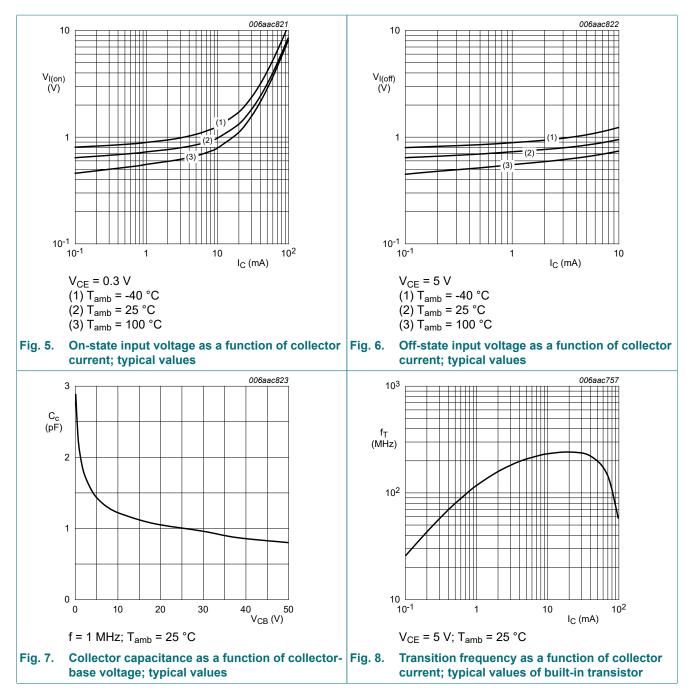
[2] Characteristics of built-in transistor.



PDTC143ZM

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11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

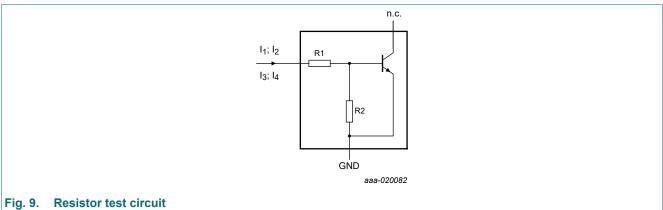
Resistor calculation

• Calculation of bias resistor 1 (R1)

$$R_1 = \frac{V(I_2) - V(I_1)}{I_2 - I_1}$$

Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I_4) - V(I_3)}{R1 \cdot (I_4 - I_3)} - 1$$

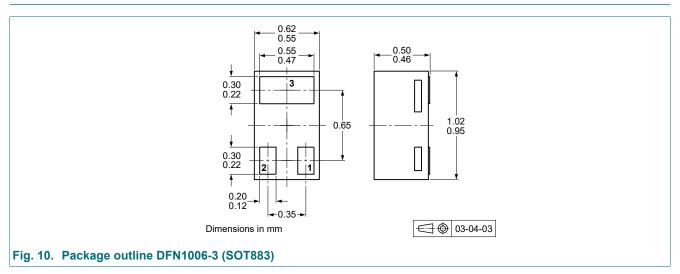


Resistor test conditions

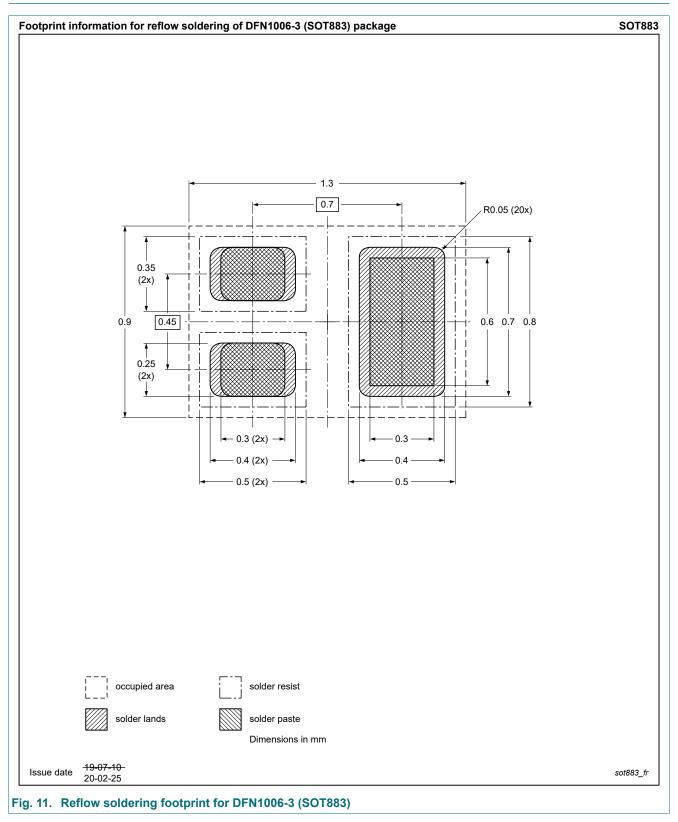
Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions				
			I ₁	l ₂	l ₃	I ₄	
PDTC143ZM	4.7	47	90 µA	140 µA	-55 µA	-105 µA	

12. Package outline



13. Soldering



14. Revision history

Table 9. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTC143ZM v.09	20230714	Product data sheet	-	PDTC143Z_SER v.8
Modification:	 The format o of Nexperia. Legal texts h 	sheet reduced to single type da f this data sheet has been rede ave been adapted to the new o mation removed.	esigned to compl	, ,,
PDTC143Z_SER v.8	20111205	Product data sheet	-	PDTC143Z_SERIES v.7
PDTC143Z_SERIES v.7	20040816	Product data sheet	-	PDTC143Z_SERIES v.6
PDTC143Z_SERIES v.6	20040406	Product specification	-	PDTC143Z_SERIES v.5
PDTC143Z_SERIES v.5	20030910	Product specification	-	PDTC143Z_SERIES v.4
PDTC143Z_SERIES v.4	20030414	Product specification	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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